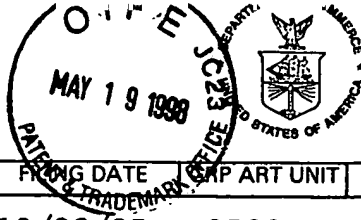


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APPLICATION NUMBER	FILING DATE	APP ART UNIT	FIL FEE REC'D	ATTORNEY DOCKET NO.	DRWGS	TOT CL	IND CL
08/997,035	12/23/97	2509	\$0.00	040301/0487	17	23	2

FOLEY & LARDNER  
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## Applicant(s)

NOBUO FUDANUKI, TOKYO, JAPAN.

Foreign Applications Japan P8-351360 12/27/96  
 Japan P9-343278 12/12/97

## TITLE

SEMICONDUCTOR INTEGRATED CIRCUIT WITH MIXED GATE ARRAY AND STANDARD CELL

PRELIMINARY CLASS: 326

(see reverse)

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DOCKETING DEPT.

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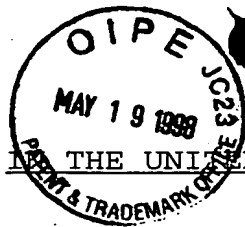
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#3

THE UNITED STATES PATENT AND TRADEMARK OFFICE

ATTY. DOCKET NO. 040301/0487

In re Patent Application of  
Nobuo FUDANUKI et al.

Serial No. 08/997,035

Group Art Unit: 2509

Filed: December 23, 1997

Examiner: Unknown

For: SEMICONDUCTOR INTEGRATED CIRCUIT WITH MIXED GATE  
ARRAY AND STANDARD CELL

REQUEST FOR CORRECTED FILING RECEIPT

Assistant Commissioner for Patents  
Washington, D.C. 20231

Sir:

It is respectfully requested that a corrected Filing Receipt be issued in connection with the above-identified application, in order to add the priority document information to read as follows:

--Foreign Applications--Japan	P8-351360	12/27/96
Japan	P9-343278	12/12/97--.

The original Filing Receipt is returned herewith.  
Kindly forward the corrected Filing Receipt to the undersigned attorney of record.

Respectfully submitted,

May 19, 1998

Date

Richard L. Schwaab  
Reg. No. 25,479

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